

IN THE SPECIFICATION:

Please amend the 13 contiguous paragraphs starting at page 1, line 5 and ending at page 2, line 6 as shown below.

--U.S. Patent Application Ser. No. 09/652,644 (~~15311-2284~~) entitled ADAPTIVE DATA PREFETCH PREDICTION ALGORITHM; ^{, now patent No. 6701387,}

U.S. Patent Application Ser. No. 09/653,133 (~~15311-2282~~) entitled UNIQUE METHOD OF REDUCING LOSSES IN CIRCUITS USING V² PWM CONTROL;

U.S. Patent Application Ser. No. 09/652,641 (~~15311-2283~~) entitled IO SPEED AND LENGTH PROGRAMMABLE WITH BUS POPULATION;

U.S. Provisional Patent Application Ser. No. 60/304,167 (~~15311-2285~~) entitled SYSTEM AND METHOD FOR USING FUNCTION NUMBERS TO INCREASE THE COUNT OF OUTSTANDING SPLIT TRANSACTIONS; ^{, now patent No 6647453,}

U.S. Patent Application Ser. No. 09/652,984 (~~15311-2286~~) entitled SYSTEM AND METHOD FOR PROVIDING FORWARD PROGRESS AND AVOIDING STARVATION AND LIVELOCK IN A MULTIPROCESSOR COMPUTER SYSTEM; ^{, now patent No 6681282,}

U.S. Patent Application Ser. No. 09/653,180 (~~15311-2287~~) entitled ONLINE ADD/REMOVAL OF SERVER MANAGEMENT INFRASTRUCTURE; ^{, now patent No 6640272}

U.S. Patent Application Ser. No. 09/652,494 (~~15311-2288~~) entitled AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD;

U.S. Patent Application Ser. No. 09/652,459 (~~15311-2289~~) entitled CLOCK FORWARDING DATA RECOVERY AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD;

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PATENTS
15311-2284
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, now patent No 6629257,
U.S. Patent Application Ser. No. 09/652,980 (15311-2290) entitled CLOCK
FORWARD INITIALIZATION AND RESET SIGNALING TECHNIQUE;

U.S. Patent Application Ser. No. 09/944,515 (15311-2292) entitled PASSIVE
RELEASE AVOIDANCE TECHNIQUE;

, now patent No 6633367,
U.S. Patent Application Ser. No. 09/652,985 (15311-2293) entitled COHERENT
TRANSLATION LOOK-ASIDE BUFFER;

, now patent No 6724850,
U.S. Patent Application Ser. No. 09/652,645 (15311-2294) entitled DETERMI-
NISTIC HARDWARE BEHAVIOR BETWEEN MULTIPLE ASYNCHRONOUS
CLOCK DOMAINS THROUGH THE NOVEL USE OF A PLL; and

, now patent No 6711693,
U. S. Patent Application Ser. No. 09/655,171 (15311-2306) entitled VIRTUAL
TIME OF YEAR CLOCK.--

Kindly delete the paragraph starting at page 40, line 25 and continuing to page 41,
line 4 as shown below.

Trademarks

~~VxWorks is a trademark of Wind River~~

~~AMD, is a trademark of Advanced Micro Devices.~~

~~Am186, is a registered trademark of Advanced Micro Devices.~~

~~Microsoft Word 97 and Microsoft PowerPoint 97 are registered trademarks of Microsoft
Corporation.~~

~~LM80, is a trademark of National Semiconductor Corporation.~~

~~I²C, is a trademark of Philips Semiconductors.~~